

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Theodore Carter Briggs and Jay Tsao

Confirmation No.:

Application No.:

Examiner:

Filing Date:

Group Art Unit:

Title: SYSTEMS AND METHODS OF PARTITIONING DATA TO FACILITATE ERROR
CORRECTION

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97(c) together with either a:
☐ Statement under 37 CFR 1.97(e), or
☐ a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- ☐ under 37 CFR 1.97 (d) together with a:
☐ Statement under 37 CFR 1.97(e)(1) or (2), and
☐ a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

☒ Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Citation together with copies, of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

☐ A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. **EU853429840US**

Date of Deposit **July 29, 2003**

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450

By

Lisa D. Jones
Typed Name: **Lisa D. Jones**

Respectfully submitted,

Theodore Carter Briggs and Jay Tsao

By

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Reg. No. **43,660**

Date: **July 29, 2003**

Telephone No.: **(216) 621-2234**

FORM PTO-1449

LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
STATEMENT

(Use several sheets if necessary)

ATTY. DOCKET NO.

200312685-1

APPLICATION NO.

CONFIRMATION NO.

APPLICANT

Theodore Carter Briggs, et al.

FILING DATE

GROUP

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	4,485,471	11/27/1984	Singh, et al.	
	1B	6,304,992 B1	10/16/2001	Cypher	
	1C	6,453,440	09/17/2002	Cypher	
	1D	6,463,506 B1	10/08/2002	McAllister, et al.	
	1E	6,477,682 B2	11/05/2002	Cypher	
	1F	6,493,843 B1	12/10/2002	Raynham	
	1G	6,519,717 B1	02/11/2003	Williams, et al.	
	1H	6,584,595 B2	06/24/2003	Cypher	
	1I	6,574,768 B2	06/03/2003	Cypher	
	1J				
	1K				

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	Timothy J. Dell, "A White Paper On The Benefits of Chipkill-Correct ECC for PC Server Main Memory", IBM Microelectronics Division, November 19, 1997, pp. 1-23
	1R	
	1S	

EXAMINER

DATE CONSIDERED